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IN THE SPECIFICATION

Please amend Page 2, Lines 9-19 as follows:

FIGURE 1 illustrates conventional band-gap reference circuit 100 according to an exemplary embodiment of the prior art. Band-gap reference circuit 100 comprises capacitor ~~[[195]] 105~~, current sources 110 and 115, amplifiers 120 and 125, N-channel transistors 131-133, resistors 140 and 145, PNP bipolar junction transistors 151-153, amplifier 160, P-channel transistor 165, and resistor 170. PNP bipolar junction transistors 151-153 are connected as diodes and are referred to hereafter as PNP diodes 151-153. According to an exemplary embodiment, PNP diode 151 has an area that is eight times larger than the area of PNP diode 152 (i.e., 8:1 ratio).

Please amend Page 2, Line 20 to Page 3, Line 6 as follows:

According to an exemplary embodiment of the present invention, controller ~~[[230]] 225~~ of cellular telephone 200 is capable of conserving power and prolonging the operating life of battery ~~[[220]] 230~~ by periodically shutting down band-gap reference circuit 240, and many of the other electrical circuits in cellular telephone 200. If the turn-on time of band-gap reference circuit 240 is made extremely short (e.g., 2 microseconds) compared to the 100+ microseconds of conventional designs, cellular telephone 200 can be powered back up without any significant delay, thereby saving considerable power over time.

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Please amend Page 20, Lines 9-20 as follows:

FIGURE 4 illustrates second order curvature correction circuit 400 for use with band-gap reference circuit 240 according to an exemplary embodiment of the present invention. The accuracy of the $V_{(bg)}$ reference voltage in FIGURE 3 may be significantly enhanced by second order curvature correction circuit 400, which injects a correction current, $I_{(CORR)}$, into the node at the emitter of PNP diode 325 in FIGURE 3. Second order curvature correction circuit 400 comprises P-channel transistors 411-413, P-channel transistors 421-423 and P-channel transistors 431-433. Second order curvature correction circuit 400 further comprises inverters 441-444, NAND gate 450, NOR gate ~~[[444]]~~ 455, and NAND gate 460.